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**Apparatus and method for performing behavioral modeling in hardware emulation and simulation environments**

US Pat. 5946472 - Filed Oct 31, 1996 - International Business Machines Corporation

While some degree of **parallel** operation of **concurrent** operations model 120 and ... 3 is a block diagram of a known **hardware emulator** environment; FIG. ...**Apparatus for improving concurrent behavior modeling with emulation**

US Pat. 6782355 - Filed Mar 10, 2000 - Quickturn Design Systems, Inc.

The chief advantage of including an **emulator/accelerator** is that it can perform the **concurrent** operations in **parallel** with the sequential operations ...**Maintenance of speculative state of parallel executed jobs in an information processing system**

US Pat. 6345351 - Filed Nov 12, 1999 - Telefonaktiebolaget LM Ericsson(publ)

The use of an **emulator** program 203 as depicted in FIG. 2 is but one way of generating and running **parallel** jobs 207 from an application program 201. ...**Dynamically configurable debug port for concurrent support of debug functions from multiple data processing cores**

US Pat. 6738929 - Filed Mar 2, 2001 - Texas Instruments Incorporated

Other components provide a **hardware** search capability designed to locate the blocks and other peripherals in the system memory map. The **emulator** software ...**Shared bus in-circuit emulator system and method**

US Pat. 5313618 - Filed Sep 3, 1992 - Metalink Corp.

Therefore, software developed using the **emulator** functions such as timers, counters, serial or **parallel** can be completely debugged, except for the **hardware** ...**Hardware and software co-verification employing deferred synchronization**

US Pat. 6356862 - Filed Sep 24, 1998

In the case of 50 emulation, the **hardware** is "realized" by configuring the reconfigurable logic and interconnect elements of the **emulator**. ...**Virtual machine monitors for scalable multiprocessors**

US Pat. 6075938 - Filed Jun 10, 1998 - The Board of Trustees of the Leland Stanford Junior University

Scalable **hardware** evolves, but what about the network OS? ... and a **hardware emulator** that creates and manages a plurality of independent virtual machines; ...**Method of measuring performance of an emulator and for adjusting emulator operation in response thereto**

US Pat. 6882968 - Filed Oct 25, 2000 - Sony Computer Entertainment Inc.

When the target's processors operate in **parallel**, an interrupt signal from ...The timing of display output and other **concurrent** pro-cesses like sound and ...**Isolating a central processing unit from the operating system controlling said unit and its associated hardware for interaction of the unit with data handling apparatus alien to the operating system**

US Pat. 5701502 - Filed Oct 19, 1994 - International Business Machines Corporation

**Concurrent** with event 'd,' as a result of 'c,' S/370 separately run task which

will format the file to any of the 55 hardware has been transferring the 16 ...

System and method for dynamic reconfigurable computing using automated translation

US Pat. 7007264 - Filed May 2, 2003 - Xilinx, Inc.

As such, the microprocessor and its program are not operating as a simulator for hardware. In the exemplary invention, the emulator interacts as if it were ...

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